



Invites Industry Experts, Faculty, and Students to join hands-on experience with

# RISC-V, TL-VERILOG, AND THE MAKERCHIP IDE

 Speaker:

**STEVE HOOVER**

FOUNDER OF REDWOOD EDA  
SHREWSBURY, MASSACHUSETTS, UNITED STATES



Episode

**TL-Verilog to create a RISC-V CPU core**



Register Here  
Limited Seats Register Early !



DEC 2, 2024  
9.30 AM TO 12.30 PM IST  
PESU - EC CAMPUS ,SEMINAR HALL 1

Steve Hoover is the founder of Redwood EDA, a Massachusetts startup specializing in emerging digital logic modeling tools and methodology. He brings with him into this role a deep understanding of the complexities of high-performance IC design, having contributed to multiple generations of Alpha microprocessors at DEC and Compaq as well as Itanium and Xeon server projects and Omni-Path HPC network switch microarchitecture for Intel.



For hands-on session we will be using online tools and Participants must bring their own laptop for hands-on activity.

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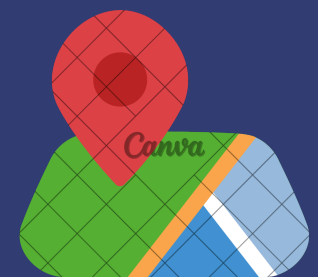
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